



- ☐ Tentative Specification
- ☐ Preliminary Specification
- ☒ Approval Specification

MODEL NO.: V236BJ1
SUFFIX: P01

Customer:

APPROVED BY

SIGNATURE

Name / Title

Note

Please return 1 copy for your confirmation with your signature and comments.

Approved By	Checked By	Prepared By
Chao-Chun Chung	Roger Huang	Chris Chen

**CONTENTS**

1. GENERAL DESCRIPTION.....	4
1.1 OVERVIEW	4
1.2 FEATURES	4
1.3 MECHANICAL SPECIFICATIONS	4
2. ABSOLUTE MAXIMUM RATINGS.....	5
2.1 ABSOLUTE RATINGS OF ENVIRONMENT	5
2.2 PACKAGE STORAGE	6
2.3 ELECTRICAL ABSOLUTE RATINGS (OPEN CELL)	6
3. ELECTRICAL CHARACTERISTICS.....	7
3.1 TFT LCD MODULE.....	7
4. BLOCK DIAGRAM OF INTERFACE.....	10
4.1 TFT LCD MODULE.....	10
5. INPUT TERMINAL PIN ASSIGNMENT	11
5.1 TFT LCD MODULE INPUT	11
5.2 BLOCK DIAGRAM OF INTERFACE.....	13
5.3 LVDS INTERFACE	14
5.4 COLOR DATA INPUT ASSIGNMENT	15
5.5 FLICKER (VCOM) ADJUSTMENT	16
6. INTERFACE TIMING	17
6.1 INPUT SIGNAL TIMING SPECIFICATIONS	17
6.2 POWER ON/OFF SEQUENCE	20
7. OPTICAL CHARACTERISTICS	21
7.1 TEST CONDITIONS	21
7.2 OPTICAL SPECIFICATIONS	22
8. DEFINITION OF LABELS	25
8.1 OPEN CELL LABEL.....	25
8.2 CARTON LABEL	25
9. PACKAGING.....	26
9.1 PACKING SPECIFICATIONS.....	26
9.2 PACKING METHOD	26
10. PRECAUTIONS.....	28
10.1 ASSEMBLY AND HANDLING PRECAUTIONS.....	28
10.2 SAFETY PRECAUTIONS	28
11. MECHANICAL CHARACTERISTIC	29

**REVISION HISTORY**

Version	Date	Page (New)	Section	Description
Ver. 2.0	Apr. 11, 2012	All	All	The Approval Specification was first issued.



1. GENERAL DESCRIPTION

1.1 OVERVIEW

V236BJ1-P01 is a 23.6" TFT Liquid Crystal Display product with driver ICs and 1ch-LVDS interface. This product supports 1366*768 HDTV format and can display 16.7M colors (8-bit / color).

1.2 FEATURES

CHARACTERISTICS ITEMS	SPECIFICATIONS
Screen Diagonal [in]	23.6
Pixels [lines]	1366 x 768
Active Area [mm]	521.4705 (H) x 293.184 (V)
Sub-Pixel Pitch [mm]	0.12725 (H) x 0.38175 (V)
Pixel Arrangement	RGB vertical stripe
Weight [g]	TYP. 670 g
Physical Size [mm]	535.06(W) × 307.34(H) × 1.83(D) Typ.
Display Mode	Transmissive mode / Normally black
Contrast Ratio	3000:1 Typ. (Typical value measure at CMI's module)
Glass thickness (Array / CF) [mm]	0.7 / 0.7
Viewing Angle (CR>20)	+88/-88(H), +88/-88(V) Typ. (CR ≥ 20) (Typical value measure at CMI's module)
Color Chromaticity	* Please refer to "color chromaticity" on p.22
Cell Transparency [%]	5.5%
Polarizer Surface Treatment	Anti-Glare coating (Haze 3.5%), Hard coating (3H)

1.3 MECHANICAL SPECIFICATIONS

Item	Min.	Typ.	Max.	Unit	Note
Weight	620	670	720	g	-
I/F connector mounting position	The mounting inclination of the connector makes the screen center within ± 0.5mm as the horizontal.				(2)

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2) Connector mounting position

2. ABSOLUTE MAXIMUM RATINGS**2.1 ABSOLUTE RATINGS OF ENVIRONMENT**

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T _{ST}	-20	+60	°C	(1)
Operating Ambient Temperature	T _{OP}	0	50	°C	(1), (2)

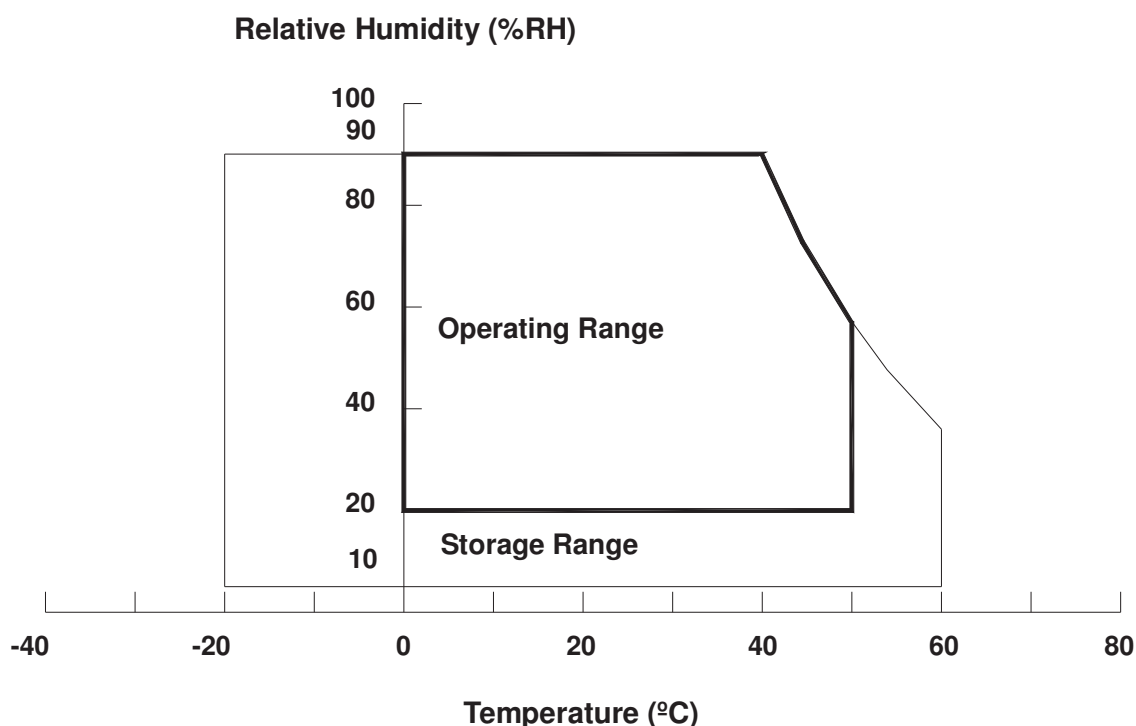
Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. ($T_a \leq 40\text{ }^{\circ}\text{C}$).

(b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40\text{ }^{\circ}\text{C}$).

(c) No condensation.

Note (2) The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 65 °C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 65 °C. The range of operating temperature may degrade in case of improper thermal management in final product design.



**2.2 PACKAGE STORAGE**

When storing modules as spares for a long time, the following precaution is necessary.

- (a) Do not leave the module in high temperature, and high humidity for a long time, It is highly recommended to store the module with temperature from 0 to 35 °C at normal humidity without condensation.
- (b) The module shall be stored in dark place. Do not store the TFT-LCD module in direct sunlight or fluorescent light.

2.3 ELECTRICAL ABSOLUTE RATINGS (OPEN CELL)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	VCC	-0.3	13.5	V	(1)
Logic Input Voltage	VIN	-0.3	3.6	V	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

3. ELECTRICAL CHARACTERISTICS

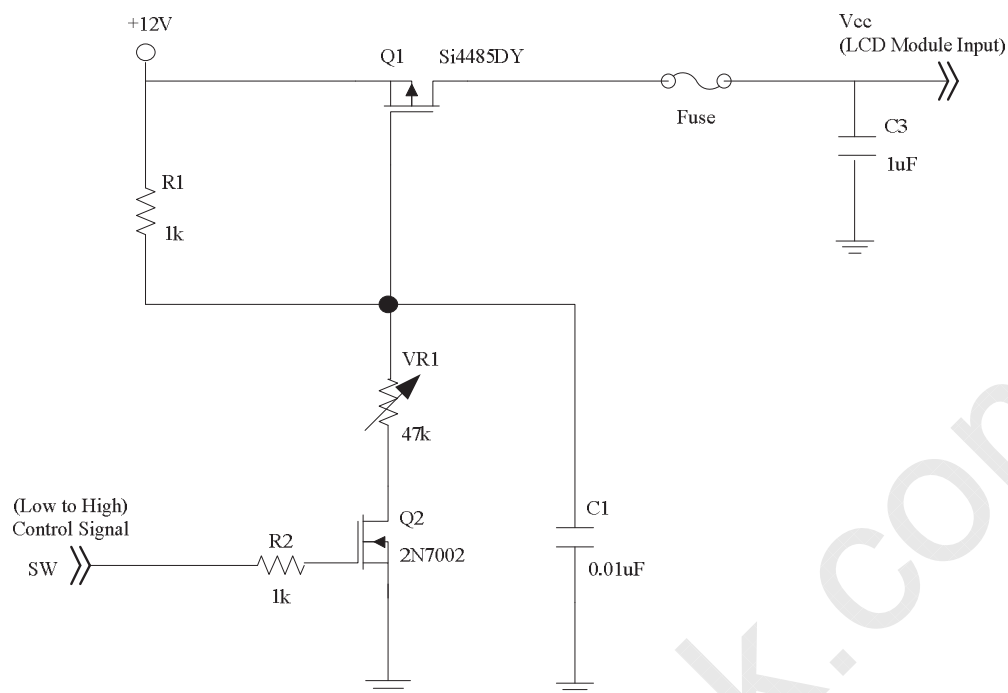
3.1 TFT LCD MODULE

(Ta = 25 ± 2 °C)

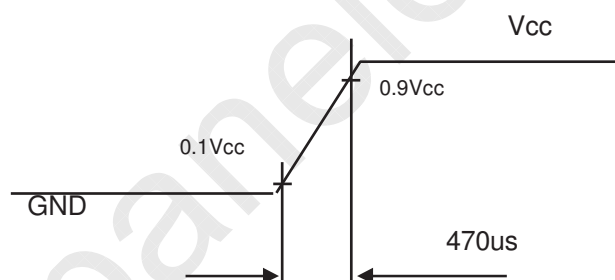
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V _{CC}	10.8	12	13.2	V	(1)
Rush Current		I _{RUSH}	—	—	3.9	A	(2)
Power consumption		P _T	—	4.44	5.772	W	(3)
Power Supply Current	White Pattern	—	—	0.35	0.455	A	(4)
	Horizontal Stripe	—	—	0.37	0.481	A	
	Black Pattern	—	—	0.21	0.273	A	
LVDS interface	Differential Input High Threshold Voltage	V _{LVTH}	+100	—	—	mV	(5)
	Differential Input Low Threshold Voltage	V _{LVTL}	—	—	-100	mV	
	Common Input Voltage	V _{CM}	1.0	1.2	1.4	V	
	Differential input voltage (single-end)	V _{ID}	200	—	600	mV	
	Terminating Resistor	R _T	—	100	—	ohm	
CMOS interface	Input High Threshold Voltage	V _{IH}	2.7	—	3.3	V	
	Input Low Threshold Voltage	V _{IL}	0	—	0.7	V	

Note (1) The module should be always operated within the above ranges.

Note (2) Measurement condition:



Vcc rising time is 470us



Note (3) The specified power supply current is under the conditions at $V_{cc} = 12\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, $f_v = 60\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



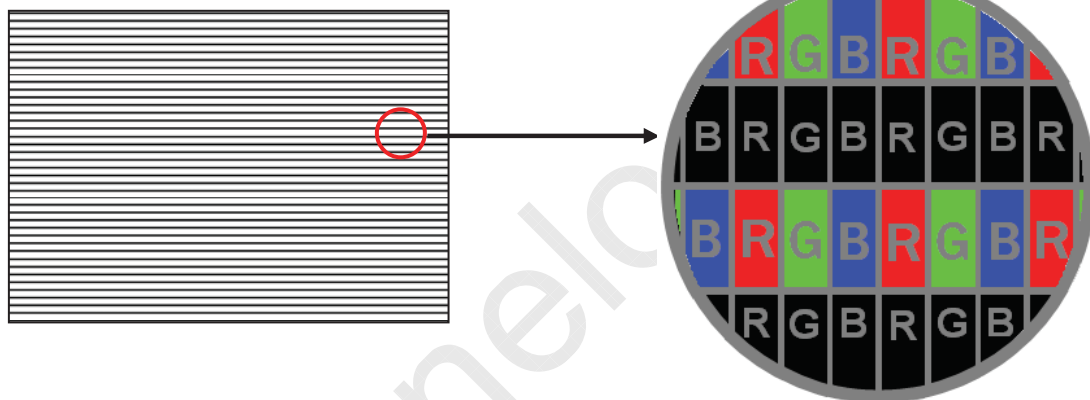
Active Area

b. Black Pattern

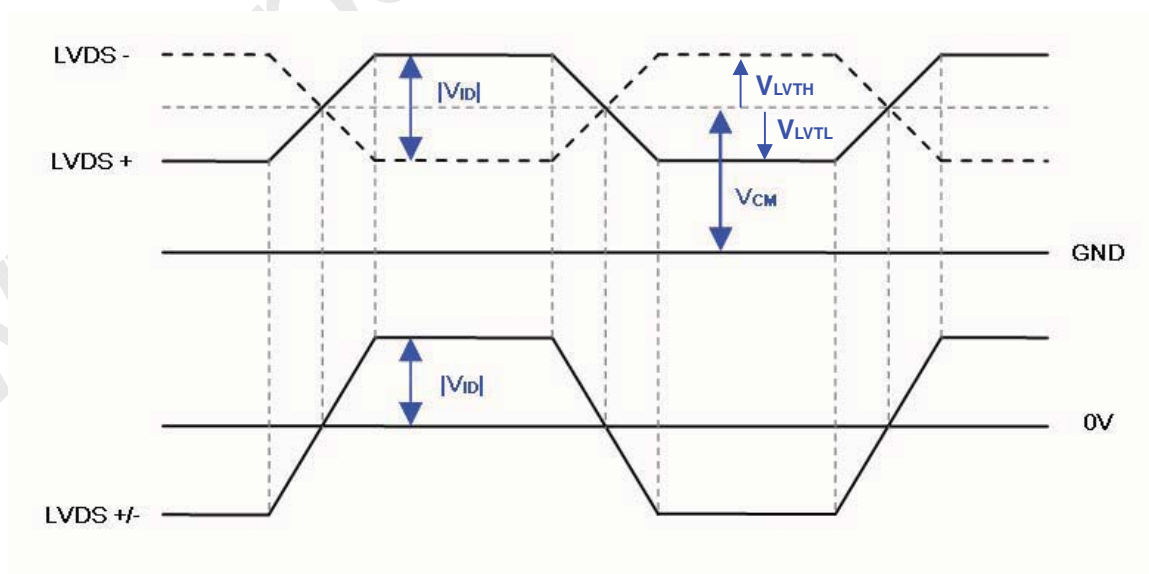


Active Area

c. Horizontal Pattern

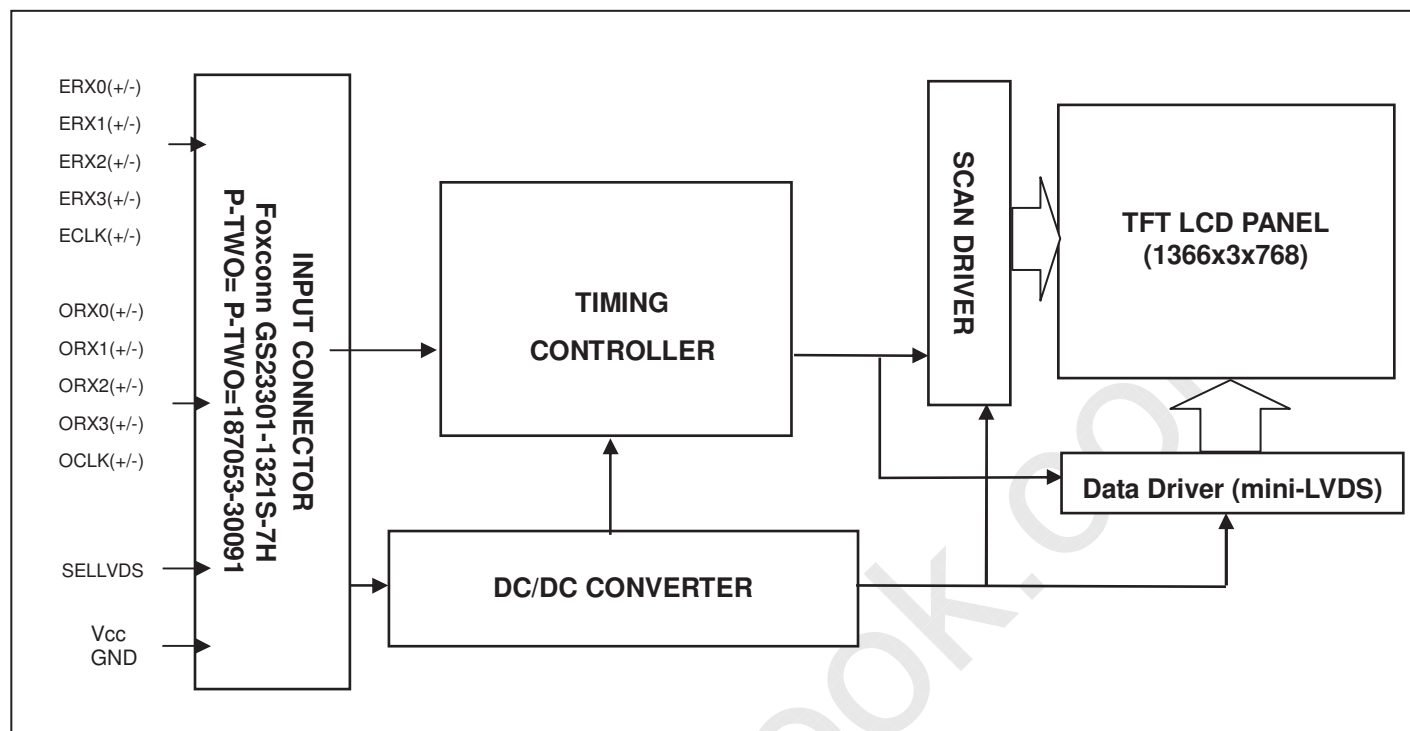


Note (4) The LVDS input characteristics are as follows:



4. BLOCK DIAGRAM OF INTERFACE

4.1 TFT LCD MODULE



5. INPUT TERMINAL PIN ASSIGNMENT

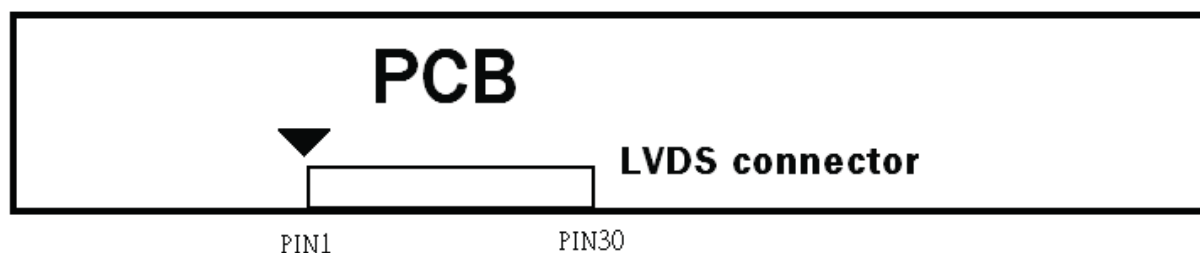
5.1 TFT LCD MODULE INPUT

Connector Pin Assignment

Pin	Name	Description	Remark
1	VCC	+12.0V power supply	
2	VCC	+12.0V power supply	
3	VCC	+12.0V power supply	
4	VCC	+12.0V power supply	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	NC	No connection	(2)
9	SELLVDS	Select LVDS Format	(3)(4)
10	NC	NC	(2)
11	GND	Ground	
12	RX0-	Negative LVDS differential data input. Channel 0	
13	RX0+	Positive LVDS differential data input. Channel 0	
14	GND	Ground	
15	RX1-	Negative LVDS differential data input. Channel 1	
16	RX1+	Positive LVDS differential data input. Channel 1	
17	GND	Ground	
18	RX2-	Negative LVDS differential data input. Channel 2	
19	RX2+	Positive LVDS differential data input. Channel 2	
20	GND	Ground	
21	RXLCK-	Negative LVDS differential clock input.	
22	RXCLK+	Positive LVDS differential clock input.	
23	GND	Ground	
24	RX3-	Negative LVDS differential data input. Channel 3	
25	RX3+	Positive LVDS differential data input. Channel 3	
26	GND	Ground	
27	NC	No connection	(2)
28	SCL	I2C clock (For Vcom tuning)	
29	SDA	I2C data (For Vcom tuning)	
30	GND	Ground	

Note (1) Connector type: (P-TWO=187053-30091 or FOXCONN=GS23302-1321S-7H)

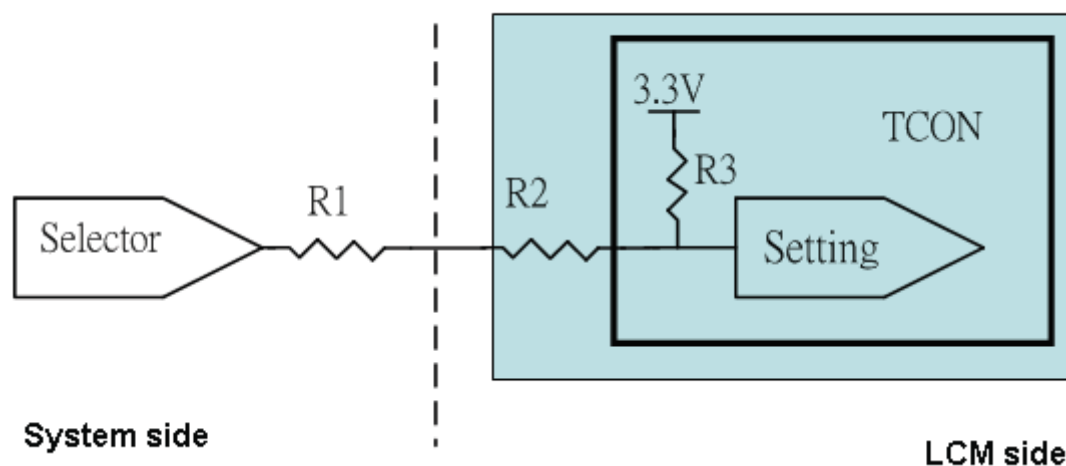
LVDS connector pin order defined as follows



Note (2) Reserved for internal use. Please leave it open.

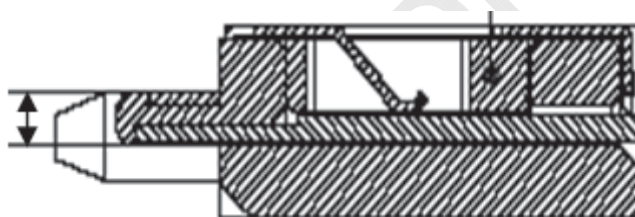
Note (3) LVDS data format Selection (0V~0.7V→VESA , 2.7V~3.3V/Open→JEDIA)

Note (4) LVDS signal pin connected to the LCM side has the following diagram. R1 in the system side should be less than 1K Ohm. ($R1 < 1K\ \Omega$)

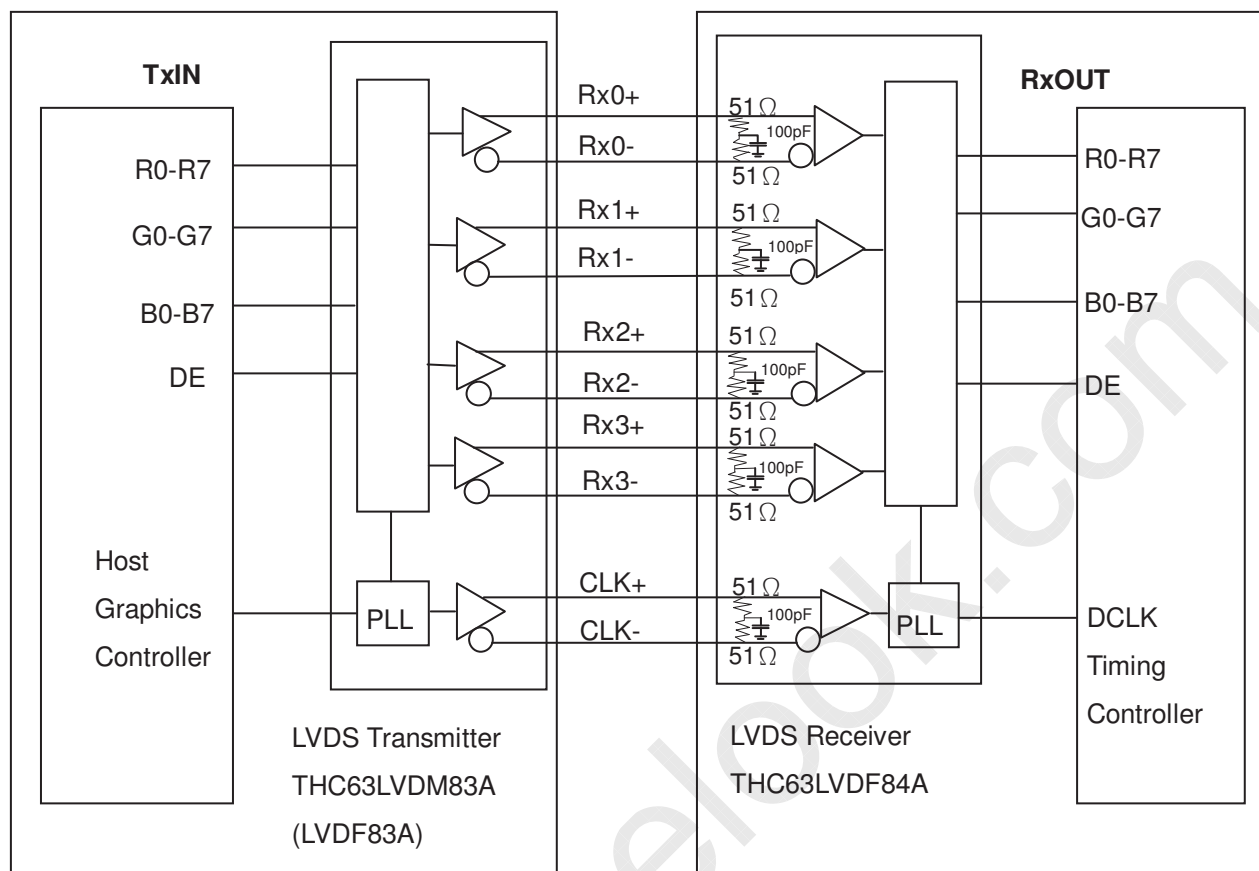


Note (5) Suggested connector connected in series : JAE FI-X30HL (Japan Aviation Electronics Ind., LTD.)

Note (6) LVDS connector mating dimension range request is 0.93mm~1.0mm as follow:



5.2 BLOCK DIAGRAM OF INTERFACE



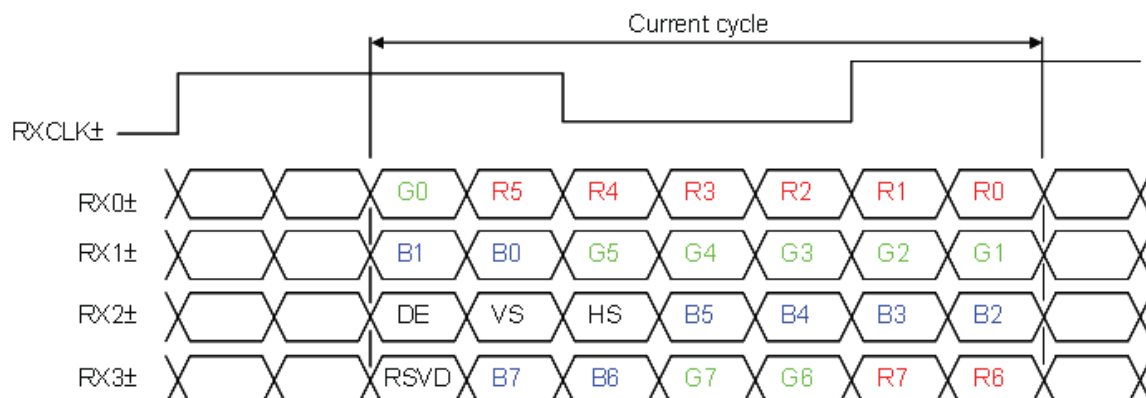
- R0~R7 : Pixel R Data
 G0~G7 : Pixel G Data
 B0~B7 : Pixel B Data
 DE : Data enable signal
 DCLK : Data clock signal

Note (1) The system must have the transmitter to drive the module.

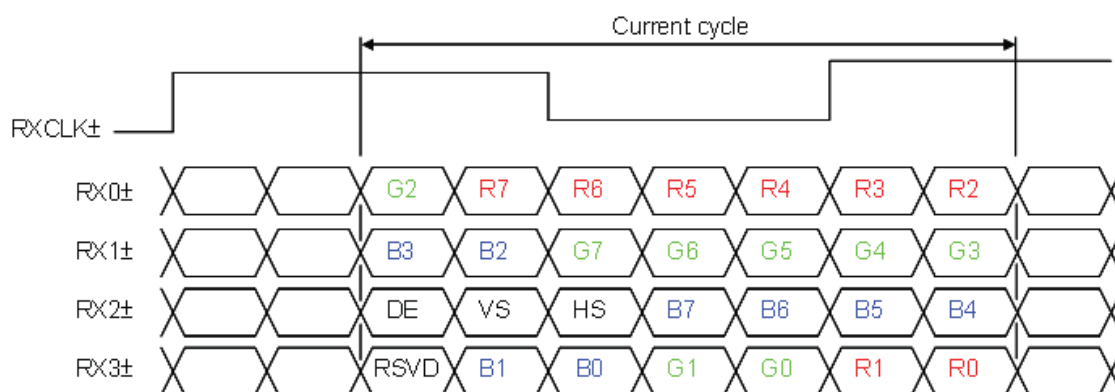
Note (2) LVDS cable impedance shall be 50 ohms per signal line or about 100 ohms per twist-pair line when it is used differentially.

5.3 LVDS INTERFACE

VESA LVDS format : (SELLVDS pin= L)



JEDIA LVDS format : (SELLVDS pin= H or Open)



R0~R7 : Pixel R Data (7; MSB, 0; LSB)

G0~G7 : Pixel G Data (7; MSB, 0; LSB)

B0~B7 : Pixel B Data (7; MSB, 0; LSB)

DE : Data enable signal

DCLK : Data clock signal

Note (1) RSVD (reserved) pins on the transmitter shall be "H" or "L".



5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of the color versus data input.

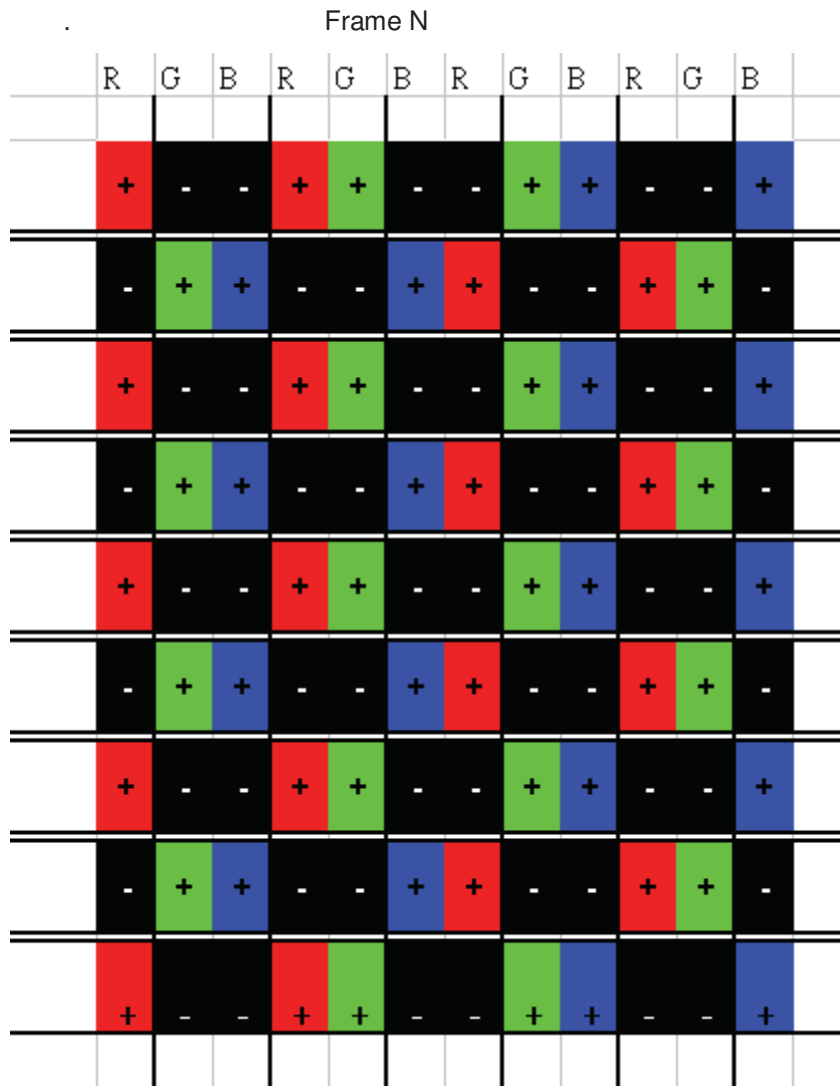
Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Red (253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Green (253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue (0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Blue (253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

5.5 FLICKER (VCOM) ADJUSTMENT

(1) Adjustment Pattern:

Flick pattern was shown as below. If customer need below pattern, please directly contact with Account FAE.



(2) Adjustment method: (Digital V-com)

Programmable memory IC is used for Digital V-com adjustment in this model. CMI provide Auto Vcom tools to adjust Digital V-com. The detail connection and setting instruction, please directly contact with Account FAE or refer CMI Auto V-com adjustment OI. Below items is suggested to be ready before Digital V-com adjustment in customer LCM line.

- USB Sensor Board.
- Programmable software.
- Document: Auto V-com adjustment suggestion OI.

6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

(Ta = 25 ± 2 °C)

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Receiver Clock	Frequency	$F_{\text{clkin}} (=1/TC)$	67.67	76	82	MHz	
	Input cycle to cycle jitter	T_{rcl}	—	—	200	ps	(2)
	Spread spectrum modulation range	$F_{\text{clkin_mod}}$	$F_{\text{clkin}}-2\%$	—	$F_{\text{clkin}}+2\%$	MHz	(3)
	Spread spectrum modulation frequency	F_{SSM}	—	—	200	KHz	
LVDS Receiver Data	Setup Time	T_{lvsu}	600	—	—	ps	
	Hold Time	T_{lvhd}	600	—	—	ps	
Vertical Active Display Term	Frame Rate	F_{r5}	47	50	53	Hz	
		F_{r6}	57	60	63	Hz	
	Total	T_{v}	776	806	1050	Th	$T_{\text{v}}=T_{\text{vd}}+T_{\text{vb}}$
	Display	T_{vd}	768	768	768	Th	
	Blank	T_{vb}	8	38	282	Th	
Horizontal Active Display Term	Total	T_{h}	1530	1560	2006	Tc	$T_{\text{h}}=T_{\text{hd}}+T_{\text{hb}}$
	Display	T_{hd}	1366	1366	1366	Tc	
	Blank	T_{hb}	164	194	640	Tc	

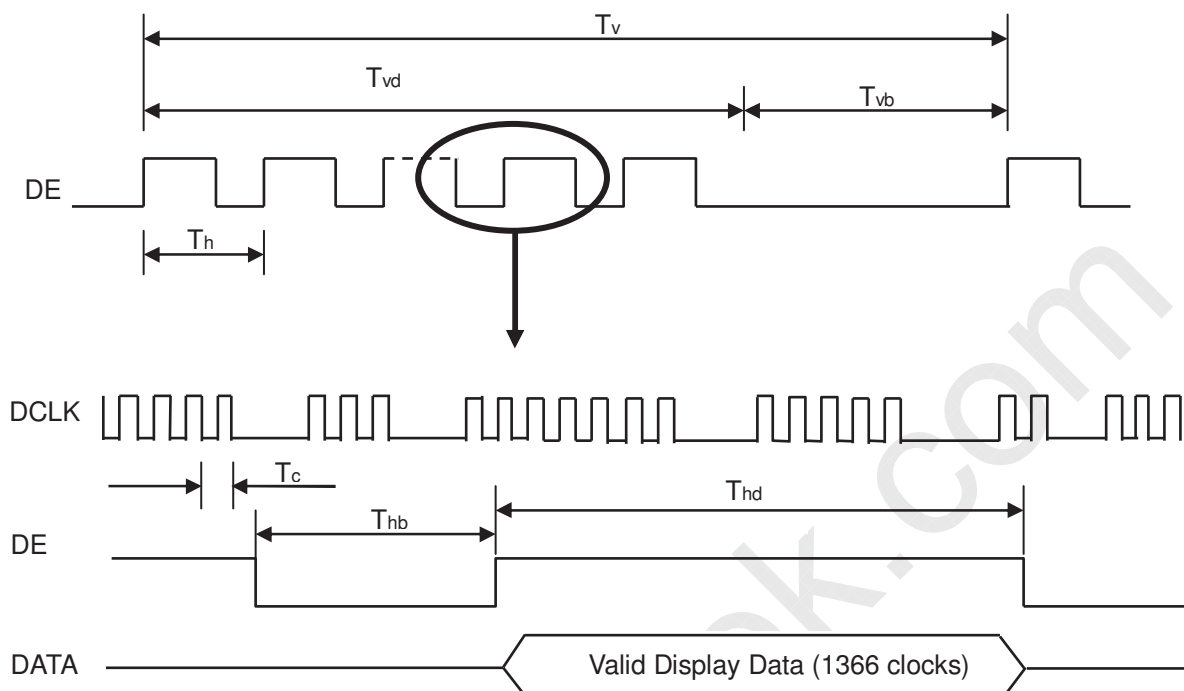
Note (1) Please make sure the range of frame rate has follow the below equation :

$$F_{\text{clkin(max)}} \geq F_{\text{r6}} \times T_{\text{v}} \times T_{\text{h}}$$

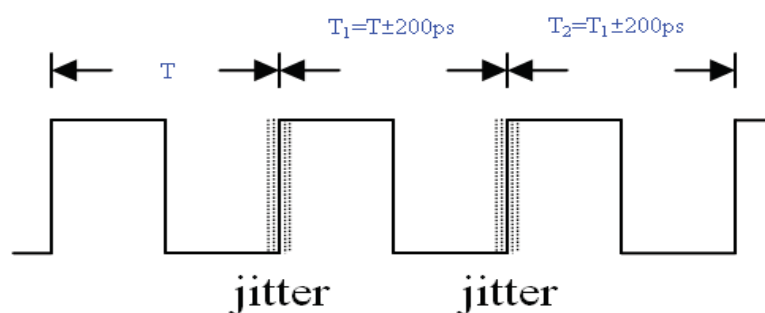
$$F_{\text{r5}} \times T_{\text{v}} \times T_{\text{h}} \geq F_{\text{clkin(min)}}$$

Note (2) This module is operated in DE only mode and please follow the input signal timing diagram below :

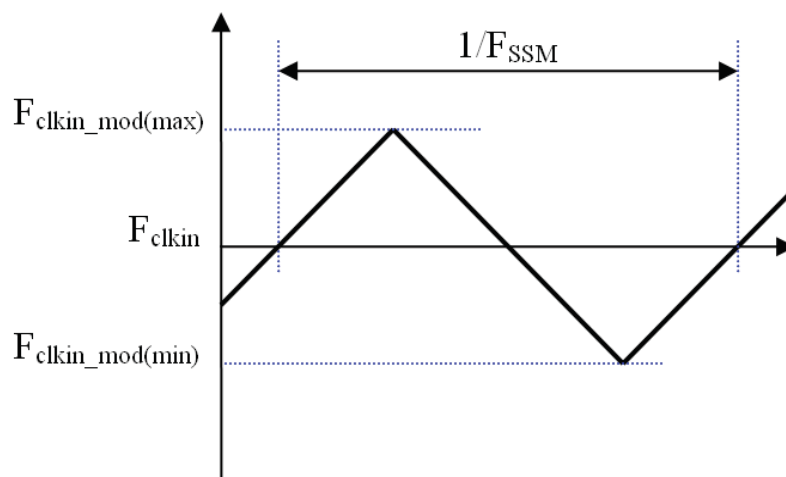
INPUT SIGNAL TIMING DIAGRAM



Note (3) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T_1 - T_1|$

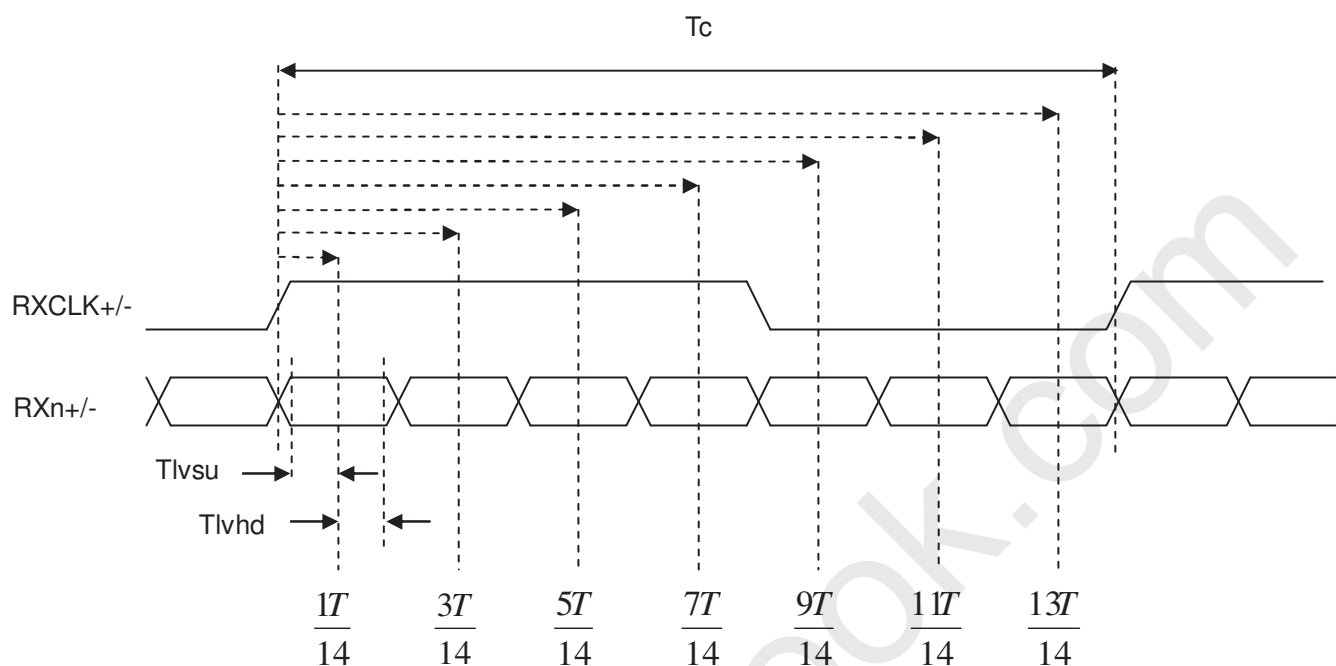


Note (4) The SSCG (Spread spectrum clock generator) is defined as below figures.



Note (5) The LVDS timing diagram and setup/hold time is defined and showing as the following figures.

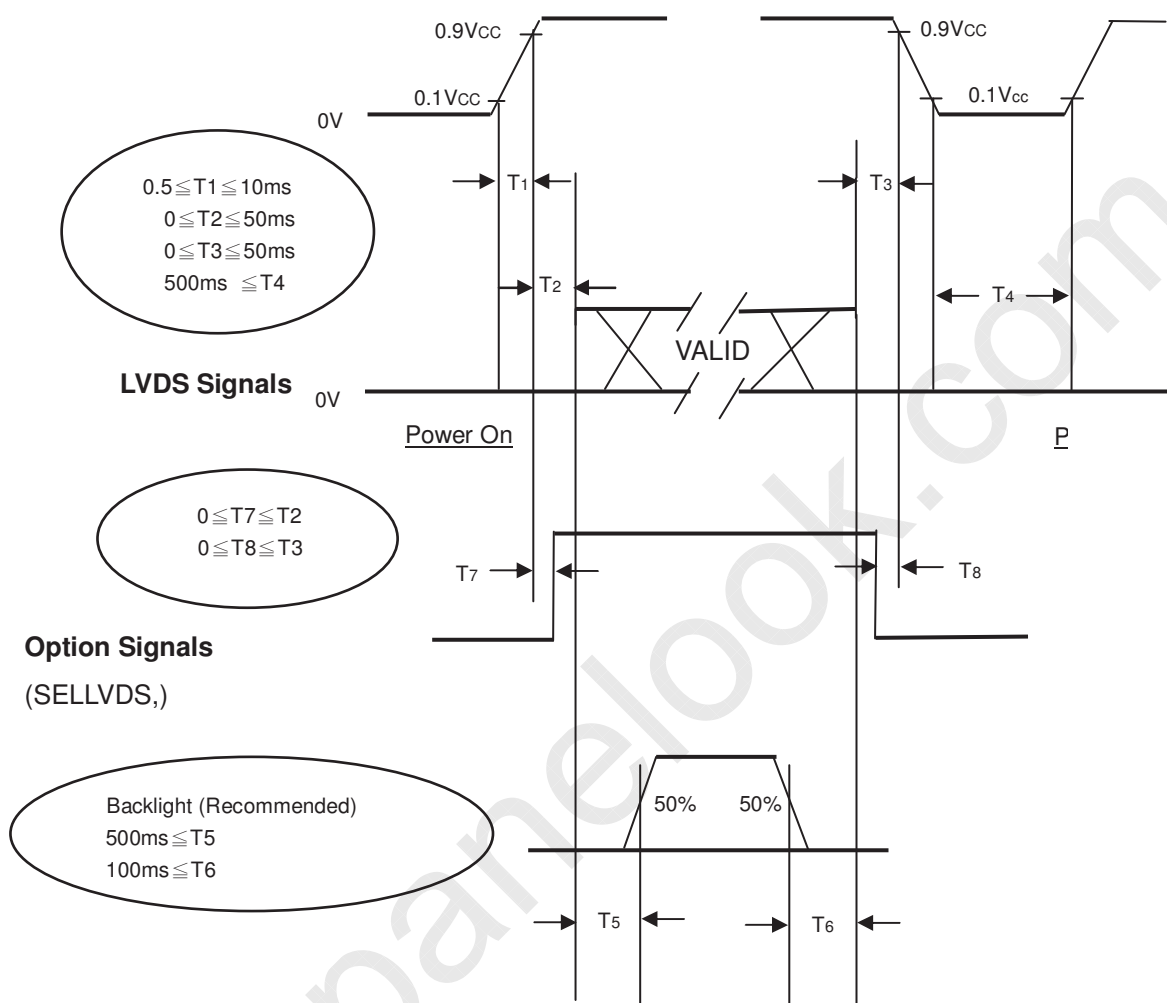
LVDS RECEIVER INTERFACE TIMING DIAGRAM



6.2 POWER ON/OFF SEQUENCE

($T_a = 25 \pm 2^\circ\text{C}$)

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

Note (1) The supply voltage of the external system for the module input should follow the definition of V_{cc} .

Note (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.

Note (3) In case of V_{CC} is in off level, please keep the level of input signals on the low or high impedance. If $T2 < 0$, that maybe cause electrical overstress failure.

Note (4) $T4$ should be measured after the module has been fully discharged between power off and on period.

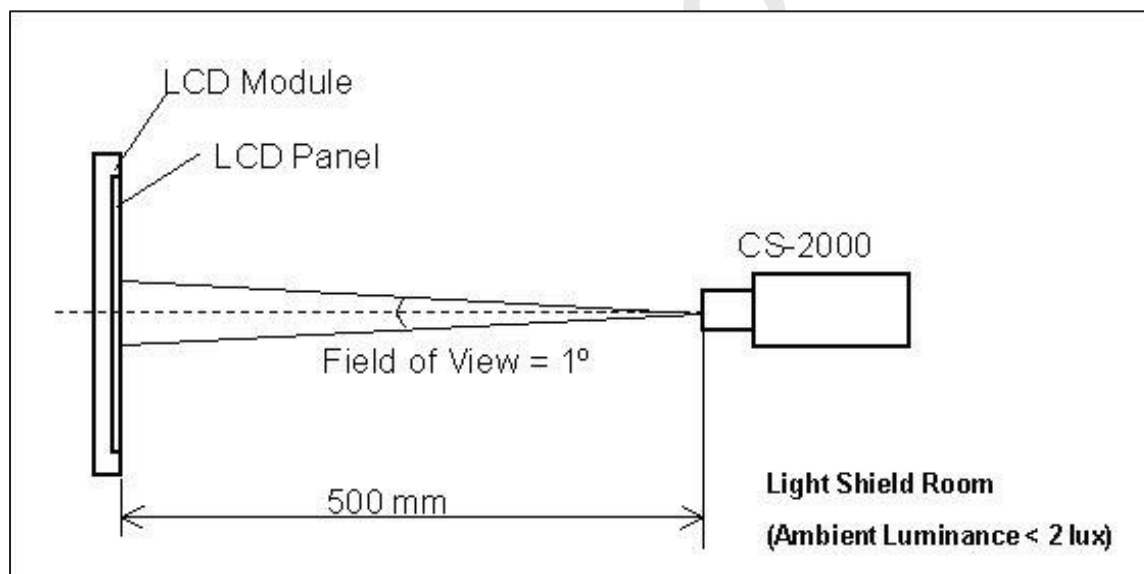
Note (5) Interface signal shall not be kept at high impedance when the power is on.

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	VCC	12V	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Lamp Current	IL	12.5±0.5	mA
Oscillating Frequency (Inverter)	FW	58±3	KHz
Vertical Frame Rate	Fr	60	Hz

The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring in a windless room.



7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown as below. The following items should be measured under the test conditions described in 7.1 and stable environment shown in 7.1.

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Color Chromaticity	Red	Rcx	$\theta_x=0^\circ, \theta_Y=0^\circ$ Viewing Angle at Normal Direction with CMI Module (V236BJ1-L01).	-	0.643	-	-	(1)
		Rcy			0.331		-	
	Green	Gcx			0.275		-	
		Gcy			0.603		-	
	Blue	Bcx			0.145		-	
		Bcy			0.063		-	
	White	Wcx			0.280		-	
		Wcy			0.290		-	
Center Transmittance		T%	$\theta_x=0^\circ, \theta_Y=0^\circ$ with CMI Module (V236BJ1-L01).	-	5.5	-	%	(1),(6)
Contrast Ratio		CR		2000	3000	-	-	(1),(3)
Response Time		Gray to gray	$\theta_x=0^\circ, \theta_Y=0^\circ$ with CMI Module (V236BJ1-L01).	-	8.5	20	ms	(1),(4)
White Variation		δW	$\theta_x=0^\circ, \theta_Y=0^\circ$ with CMI Module (V236BJ1-L01).	-	-	1.42	-	(1),(5)
Viewing Angle	Horizontal	θ_{x+}	with CMI Module (V236BJ1-L01).	80	88	-	Deg.	(1),(2)
		θ_{x-}		80	88	-		
	Vertical	θ_{Y+}		80	88	-		
		θ_{Y-}		80	88	-		

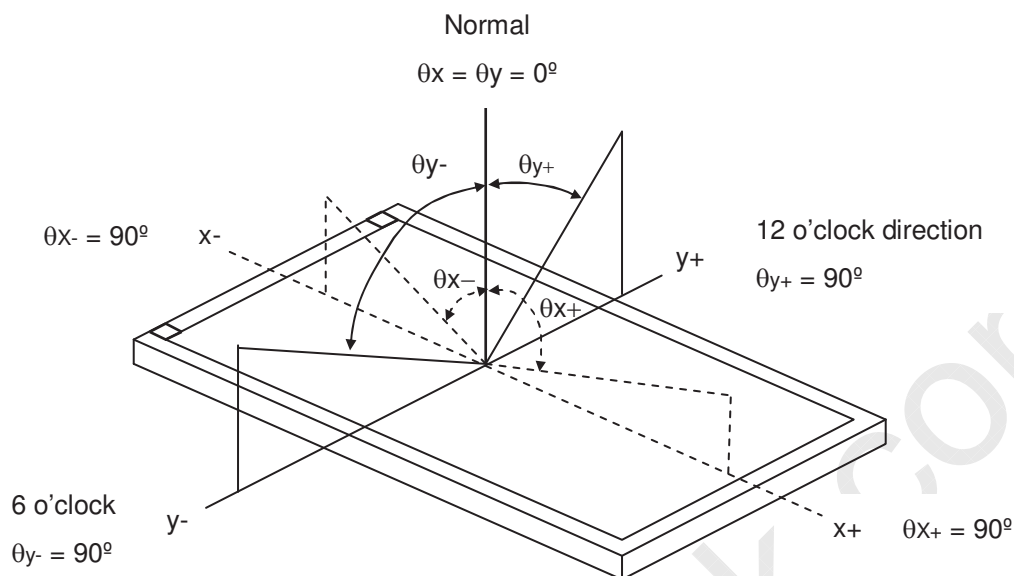
Note Light source is the standard light source "C" which is defined by CIE and driving voltage are based on suitable gamma voltages. The calculating method is as following:

1. Measure Module's and BLU's spectrum at center point. White is without signal input and R,G,B are with signal input. BLU (for V236BJ1-L01) is supplied by CMI.
2. Calculate cell's spectrum.
3. Calculate cell's chromaticity by using the spectrum of standard light source "C".

Note (1) Light source is the BLU which supplied by CMI and driving voltage are based on suitable gamma voltages.

Note (2) Definition of Viewing Angle (θ_x , θ_y):

Viewing angles are measured by Autronic Conoscope Cono-80



Note (3) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

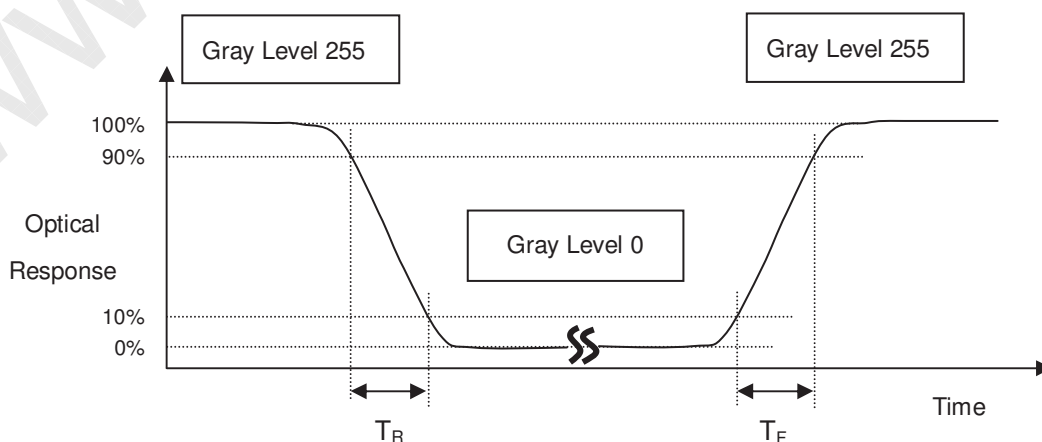
$$\text{Contrast Ratio (CR)} = \frac{\text{Surface Luminance of L255}}{\text{Surface Luminance of L0}}$$

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (5).

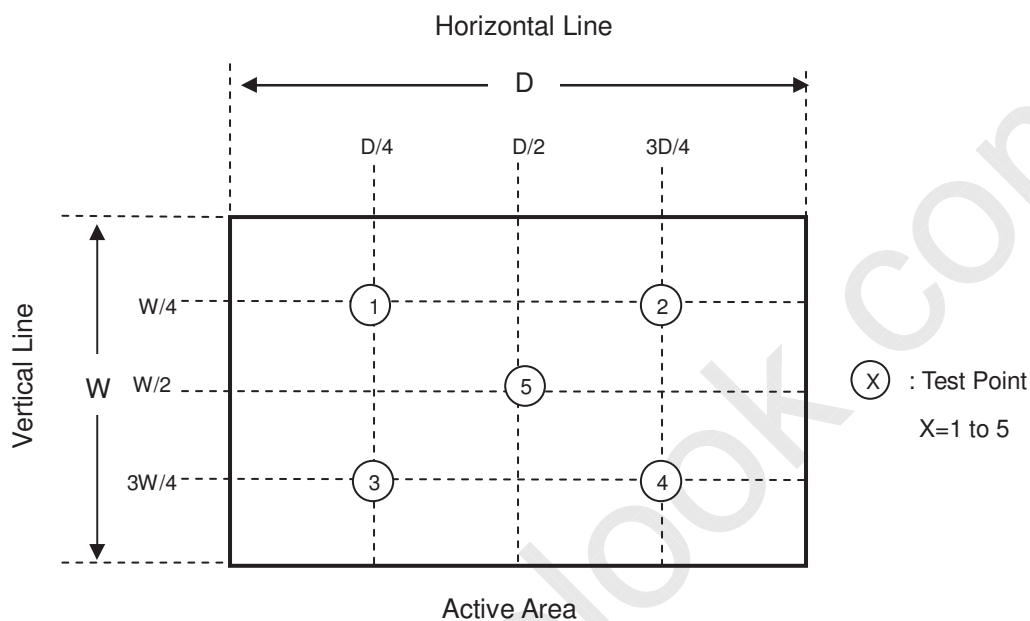
Note (4) Definition of Response Time (T_R , T_F):



Note (5) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 5 points

$$\delta W = \text{Maximum [L (1), L (2), L (3), L (4), L (5)]} / \text{Minimum [L (1), L (2), L (3), L (4), L (5)]}$$



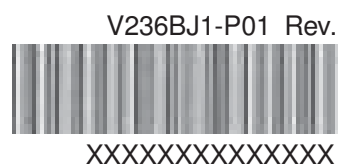
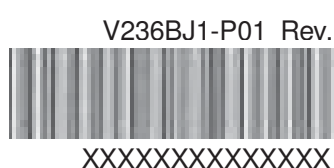
Note (6) Definition of Transmittance (T%) :

Measure the luminance of gray level 255 at center point of LCD module.

$$\text{Transmittance (T\%)} = \frac{\text{Luminance of LCD module}}{\text{Luminance of backligh unit}} \times 100\%$$

8. DEFINITION OF LABELS**8.1 OPEN CELL LABEL**

The barcode nameplate is pasted on each open cell as illustration for CMI internal control.

**8.2 CARTON LABEL**

The barcode nameplate is pasted on each box as illustration, and its definitions are as following explanation

P.O. NO. _____	Made in Taiwan
Parts ID. _____	Quantities <u>24</u>
Model Name <u>V236BJ1-P01 Rev.</u>	
Carton ID. _____	
	RoHS
XXXXXXXXXXXXXXXXXX	

P.O. NO. _____	Made in China
Parts ID. _____	Quantities <u>24</u>
Model Name <u>V236BJ1-P01 Rev.</u>	
Carton ID. _____	
	RoHS
XXXXXXXXXXXXXXXXXX	

- (a) Model Name: V236BJ1- P01
- (b) Carton ID: CMI internal control
- (c) Quantities: 24

9. PACKAGING

9.1 PACKING SPECIFICATIONS

- (1) 24 LCD TV Panels / 1 Box
- (2) Box dimensions : 670 (L) X 575 (W) X 325 (H) mm
- (3) Weight : approximately 25Kg (24 panels per box)

9.2 PACKING METHOD

Figures 9-1 and 9-2 are the packing method

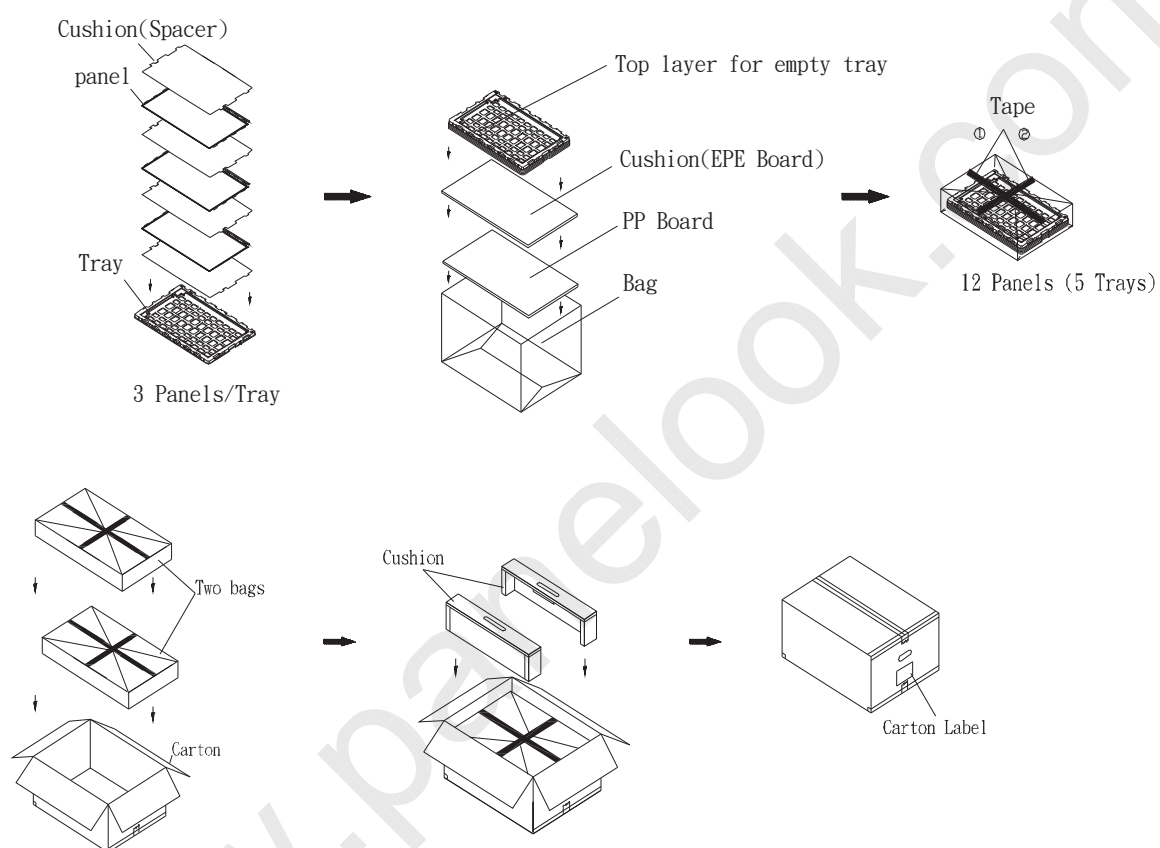


Figure.9-1 packing method

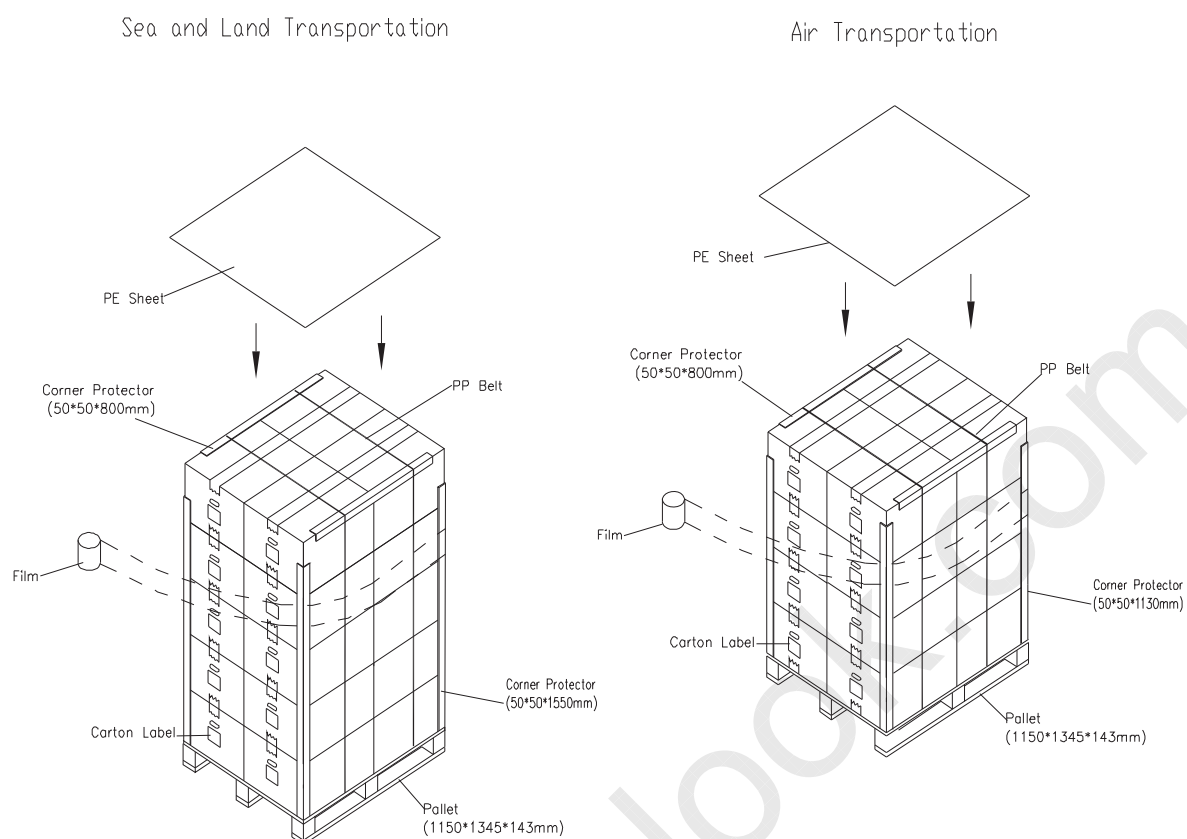


Figure.9-2 packing method

10. PRECAUTIONS**10.1 ASSEMBLY AND HANDLING PRECAUTIONS**

- (1) Do not apply rough force such as bending or twisting to the product during assembly.
- (2) To assemble backlight or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel will be damaged.
- (4) Always follow the correct power sequence when the product is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (7) It is dangerous that moisture come into or contacted the product, because moisture may damage the product when it is operating.
- (8) High temperature or humidity may reduce the performance of module. Please store this product within the specified storage conditions.
- (9) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

10.2 SAFETY PRECAUTIONS

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the product's end of life, it is not harmful in case of normal operation and storage.

11. MECHANICAL CHARACTERISTIC

